

IN THE ABSTRACT:

Please amend the Abstract as follows:

The invention provides a method and a A circuit arrangement for compensating for disturbances in a signal generated by means of discrete multitone modulation~~[,]] the signal generated by means of discrete multitone modulation exhibiting in the frequency domain a multiplicity of carrier frequencies which are used for transmitting data via a transmission channel, and each carrier frequency exhibiting a signal vector ( $a_n^{'}, b_n^{'}), comprising a multiplicity of first adder circuits (18, 19), the includes a multiplicity of first adder circuits being that are supplied with a first an error signal vector and the that is not used for transmitting data via the transmission channel. The multiplicity of first adder circuits (18, 19) adding adds the first error signal vector to at least one first signal vector ( $a_n^{'}, b_n^{'})) in order to generate an error-corrected first signal vector ( $a_n^{'}, b_n^{'}); and a multiplicity of first multiplier circuits (14, 15, 16, 17) which precede the . The circuit arrangement can also include at least one further multiplicity of adder circuits that are supplied with a further error signal vector, the at least one further multiplicity of first adder circuits (18, 19) and multiply the first adding the respective further error signal vector to the at least one signal vector to generate a progressively error-corrected signal vector. The circuit arrangement further includes at least one multiplicity of multiplier circuits that precede the at least one multiplicity of adder circuits and multiply the respective error signal vector by adjustable coefficients ( $C_{aa}^{(n)}, C_{ba}^{(n)}, C_{bb}^{(n)}, C_{ab}^{(n)})$ ), the first error signal$$$~~

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~~vector being a signal vector ( $a_r$ ,  $b_r$ ) of a carrier frequency which is not used for transmitting data via the transmission channel.~~

A Replacement Sheet for the Abstract is attached hereto.